

DEVELOPMENT BOARD



RV-8523-C3

Low-Power Real Time Clock / Calendar Module

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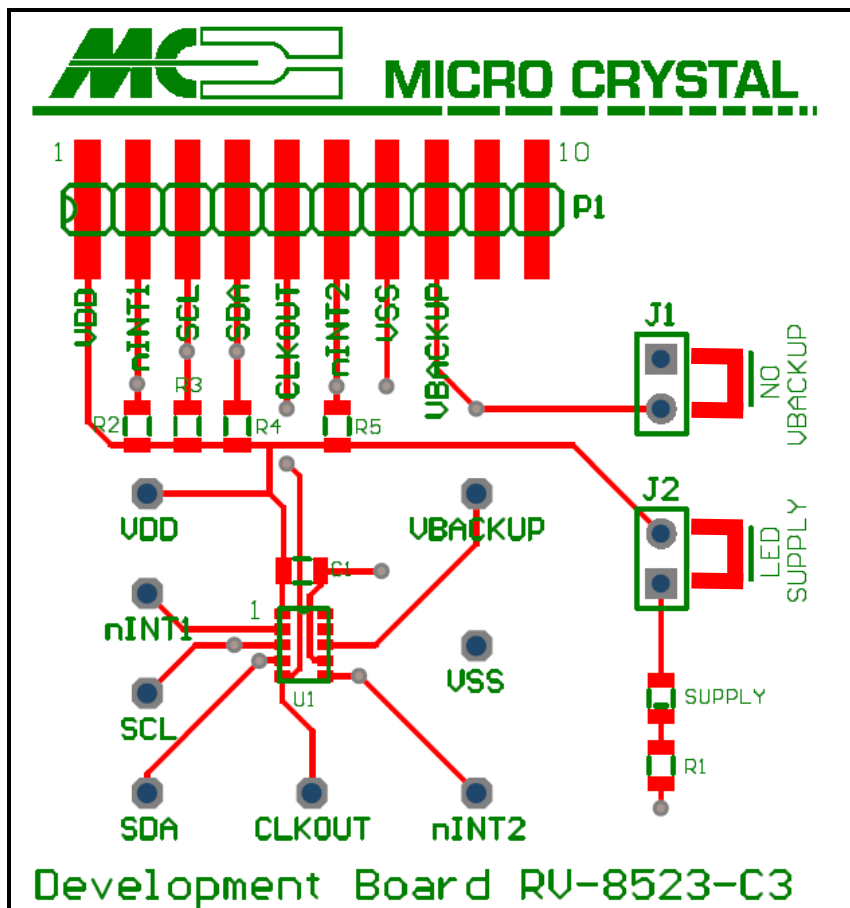
RV-8523-C3

The RV-8523-C3 is soldered onto the Development Board.
 Every pin is either accessible at test pins 1 – 10 or at the test vias situated around the device.

The following passive components are already soldered on the Board:

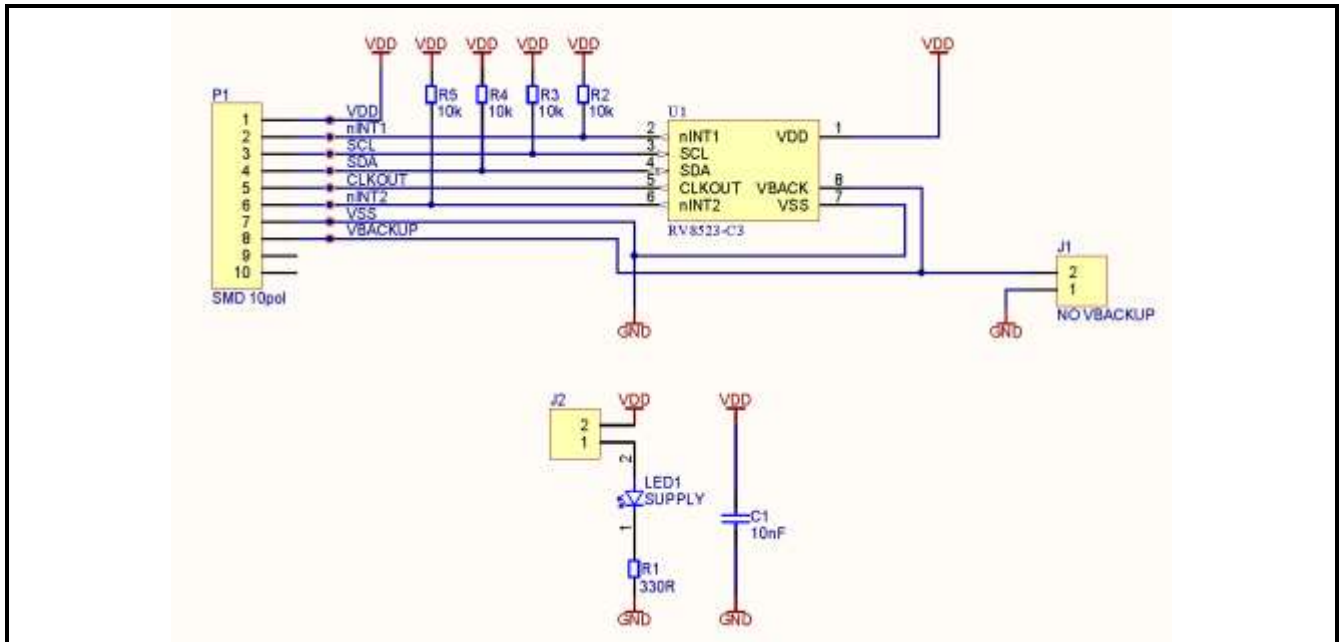
C1	10 nF	Decoupling capacitor between V _{SS} and V _{DD}
R1	330 Ω	current limiting resistor for LED
LED	green	Supply, current consumption of the LED has to be considered
R2	10 k Ω	Pull-up resistor INT_1 to V _{DD}
R3	10 k Ω	Pull-up resistor SCL to V _{DD}
R4	10 k Ω	Pull-up resistor SDA to V _{DD}
R5	10 k Ω	Pull-up resistor INT_2 to V _{DD}

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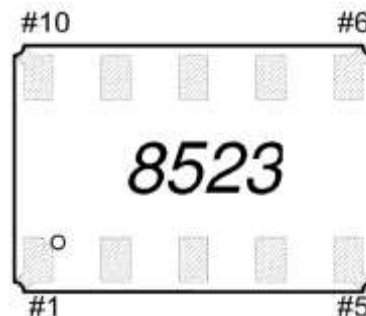


- JUMPER 1
- V_{BACKUP} to Gnd
- JUMPER 2
- Power to LED

SCHEMATICS



PINOUT RV-8523-C3



# 1 V _{DD}	# 10 N.C.
# 2 $\overline{\text{INT_2}}$	# 9 N.C.
# 3 SCL	# 8 V _{BACKUP}
# 4 SDA	# 7 V _{SS}
# 5 CLKOUT	# 6 $\overline{\text{INT_2}}$

PIN DESCRIPTION

Symbol	Pin #	Description
V _{DD}	1	Power Supply Voltage
$\overline{\text{INT_1}}$	2	Interrupt_1 Output pin (active LOW); open-drain; requires pull-up resistor
SCL	3	Serial Clock Input pin; requires pull-up resistor
SDA	4	Serial Data Input-Output pin; requires pull-up resistor
CLKOUT	5	Clock Output pin; open-drain; requires pull-up resistor
$\overline{\text{INT_2}}$	6	Interrupt_2 Output pin (active LOW); open-drain; requires pull-up resistor
V _{SS}	7	Ground
V _{BACKUP}	8	Backup Supply Voltage; tie to GND when not using backup supply voltage
NC	9	Not Connected
NC	10	Not Connected

Datasheet and Application-Manual are available for download under: www.microcrystal.com