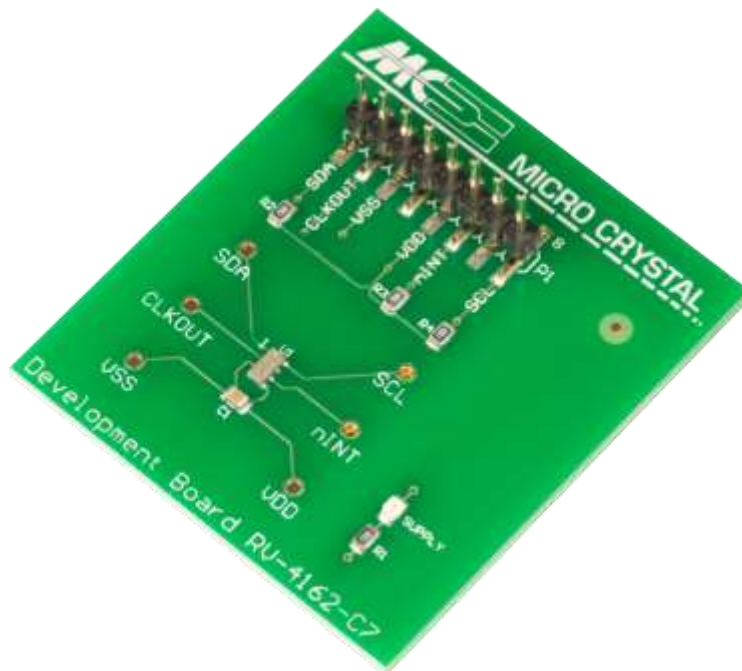


# DEVELOPMENT BOARD



# RV-4162-C7

## Miniature Real Time Clock / Calendar Module

## RV-4162-C7

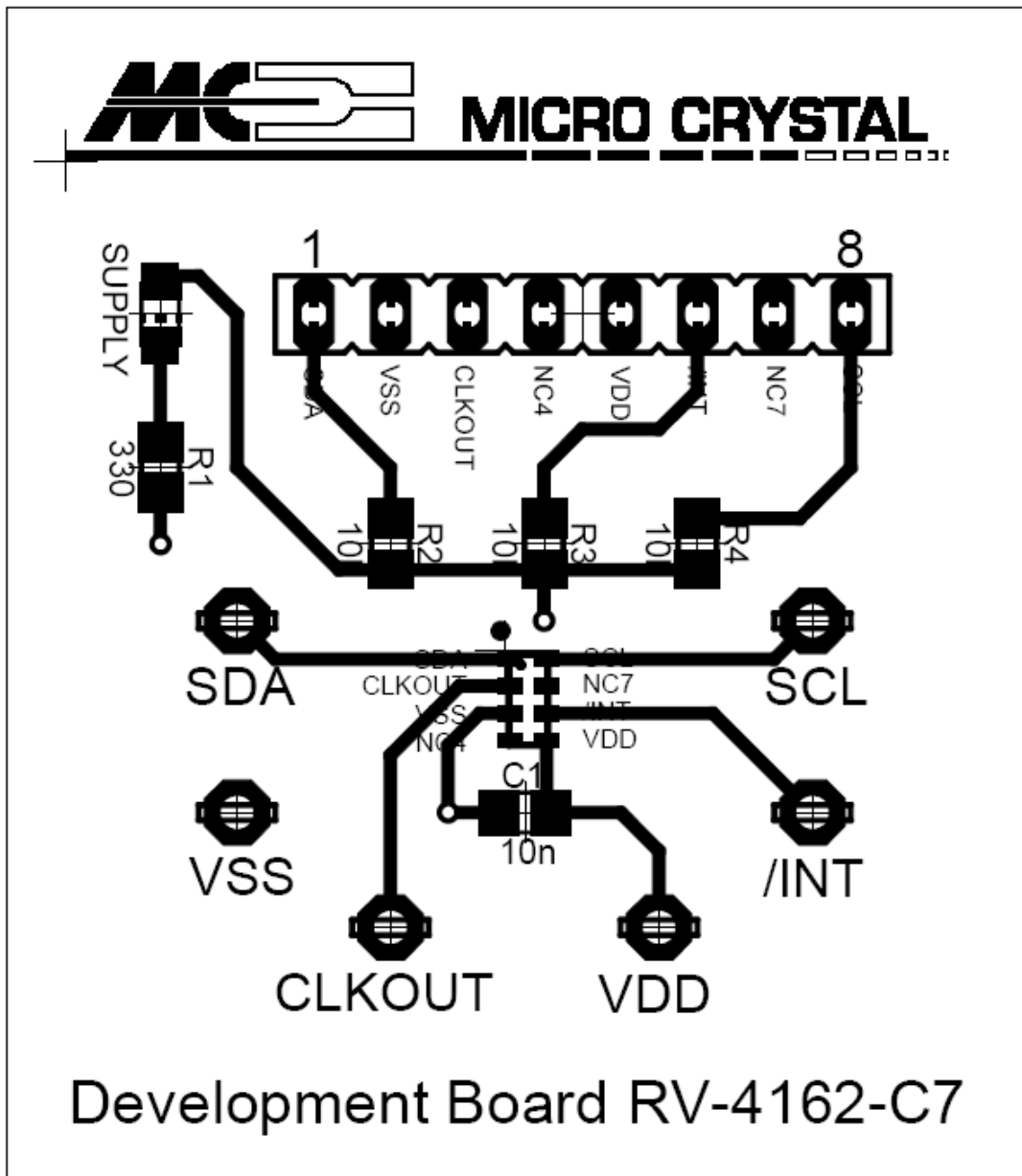
The RV-4162-C7 is soldered onto the Development Board.

Every pin is either accessible at test pins 1 – 8 or at the test vias situated around the device.

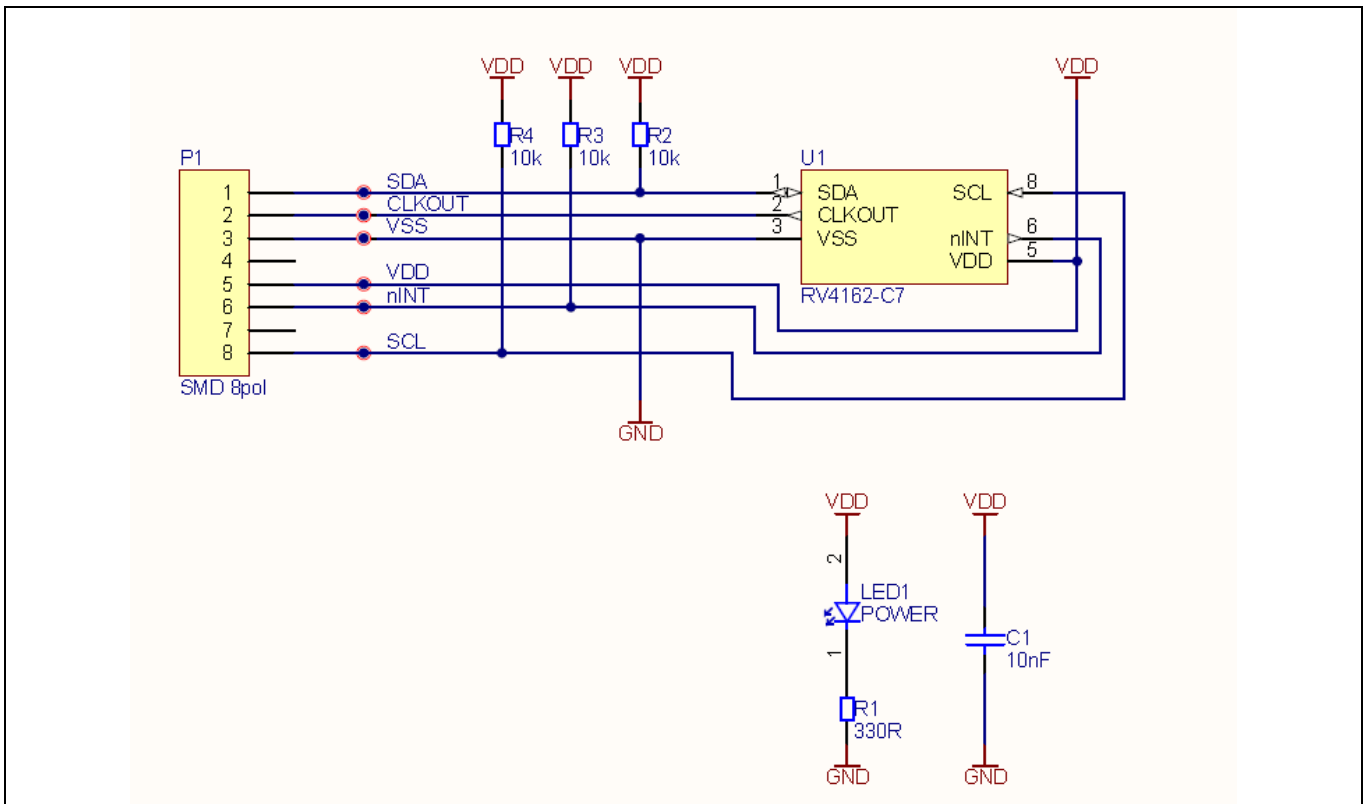
The following passive components are already soldered on the Board:

C1	10 nF	Decoupling capacitor between $V_{SS}$ and $V_{DD}$
R1	330 $\Omega$	current limiting resistor for LED
LED	green	Supply, current consumption of the LED has to be considered
R2	10 k $\Omega$	Pull-up resistor SDA to $V_{DD}$
R3	10 k $\Omega$	Pull-up resistor INT to $V_{DD}$
R4	10 k $\Omega$	Pull-up resistor SCL to $V_{DD}$

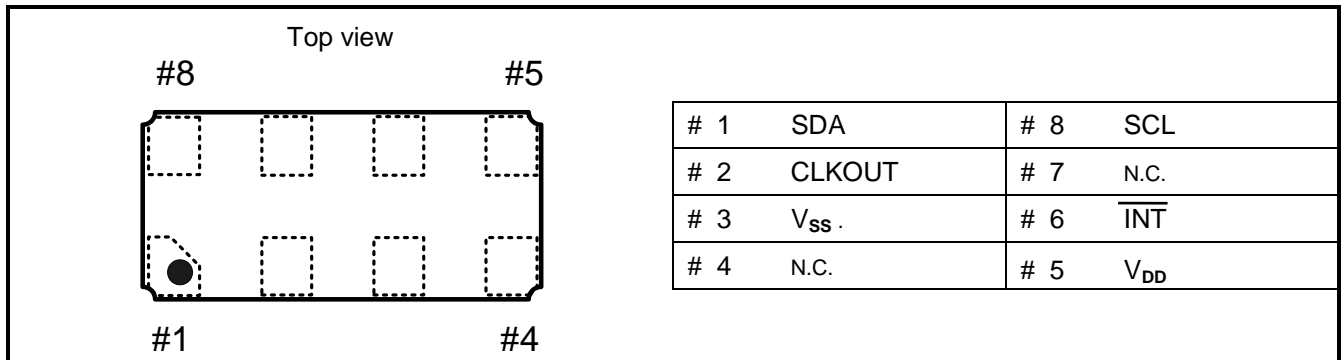
### DEVELOPMENT BOARD



SCHEMATICS



PINOUT RV-4162-C7



PIN DESCRIPTION

Symbol	Pin #	Description
SDA	1	Serial Data Input-Output pin; open-drain; requires pull-up resistor.
CLKOUT	2	Clock Output pin; push-pull output; at power-up by default 32.768kHz
V <sub>SS</sub>	3	Ground
NC	4	Not Connected
V <sub>DD</sub>	5	Positive supply voltage; recommend 10 nF decoupling capacitor close to device
$\overline{\text{INT}}$	6	Interrupt Output pin; open-drain; active LOW
NC	7	Not Connected
SCL	8	Serial Clock Input pin; requires pull-up resistor