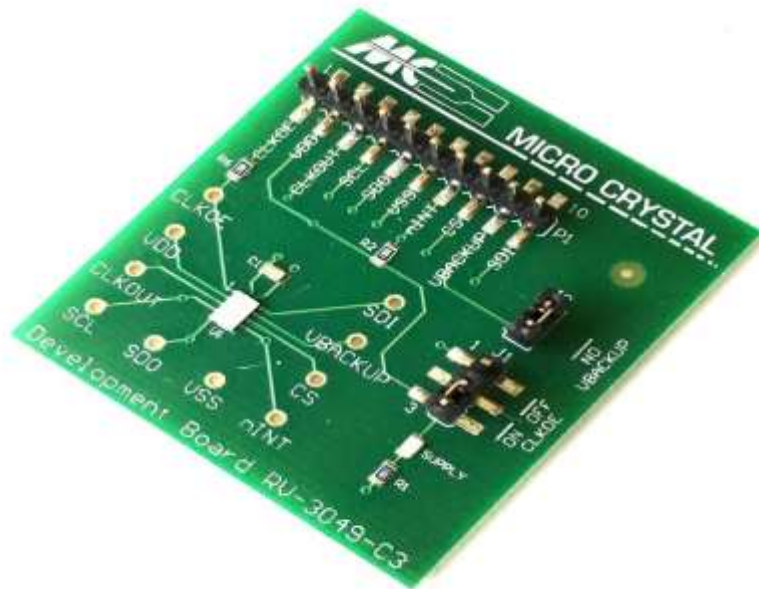


# DEVELOPMENT BOARD



# RV-3049-C3

Temperature Compensated Real-Time Clock / Calendar Module

### RV-3049-C3

The RV-3049-C3 is soldered onto the Development Board.

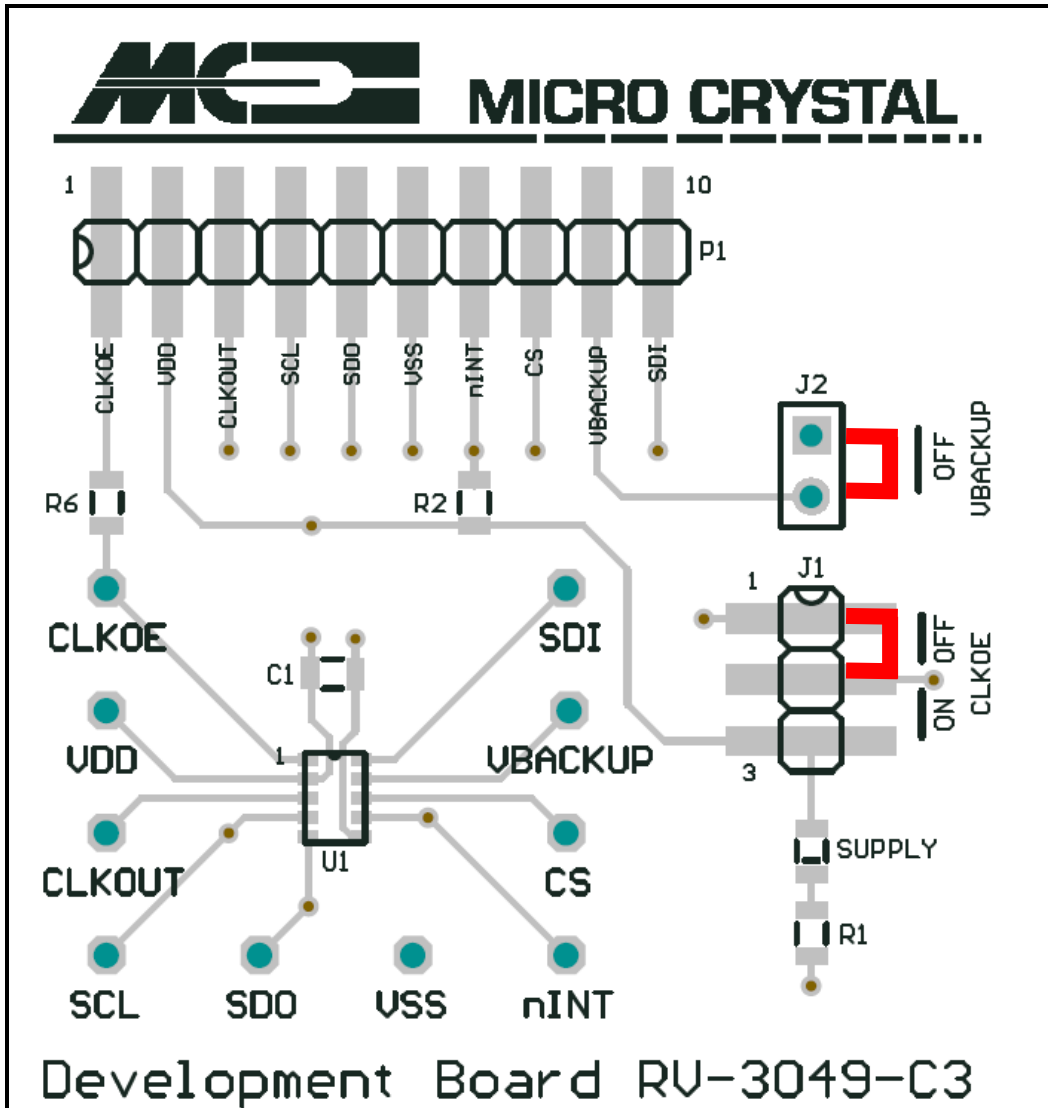
Every pin is either accessible at test pins 1 – 10 or at the test vias situated around the device.

§

The following passive components are already soldered on the Board:

- |      |               |   |
|------|---------------|---|
| C1   | 10 nF         | Decoupling capacitor between $V_{SS}$ and $V_{DD}$                                      |
| R1   | 330 $\Omega$  | current limiting resistor for LED   |
| LED1 | green         | Supply, current consumption of the LED has to be considered                             |
| R2   | 10 k $\Omega$ | Pull-up resistor INT to $V_{DD}$  |
| R6   | 10 k $\Omega$ | Protection resistors to prevent short-circuit between external CLKOE signal and jumper. |

#### DEVELOPMENT BOARD



JUMPER 2

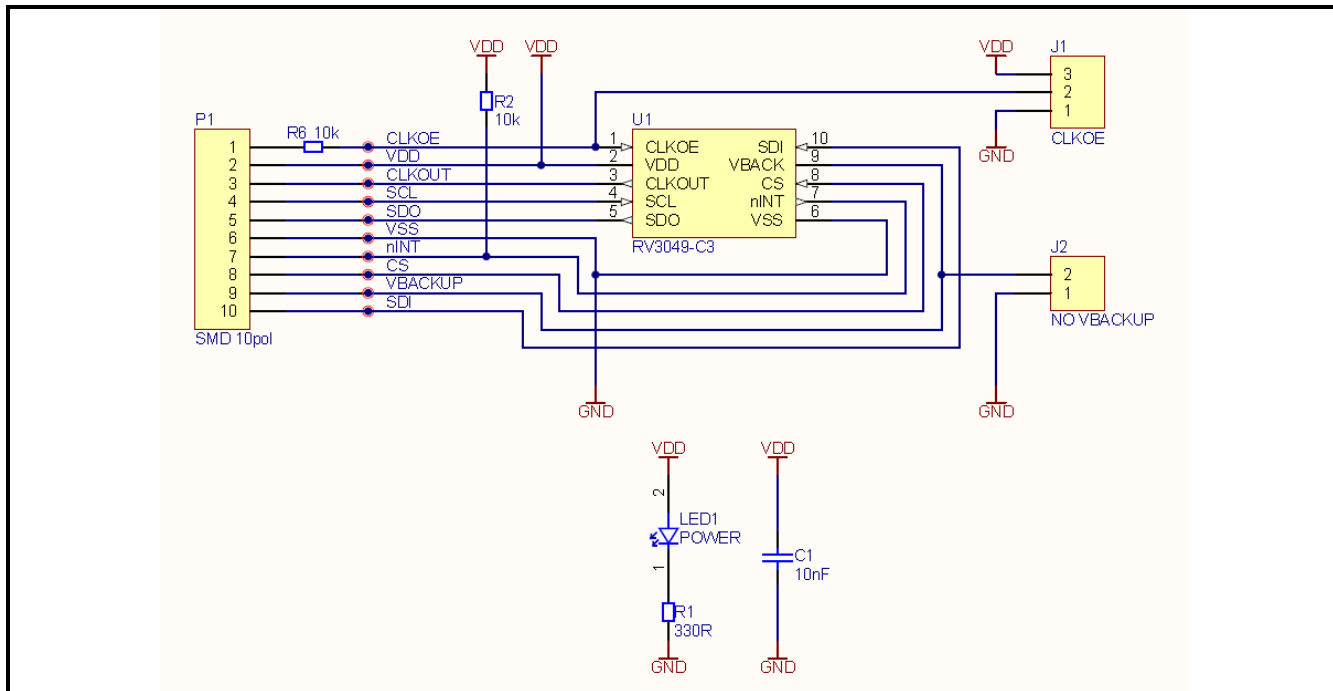
$V_{BACKUP} = GND$

JUMPER 1

CLKOE = LOW

CLKOE = HIGH

**SCHEMATICS**



**PINOUT RV-3049-C3**

# 1	CLKOE	# 10	SDI
# 2	V <sub>DD</sub>	# 9	V <sub>BACKUP</sub>
# 3	CLKOUT	# 8	CE
# 4	SCL	# 7	$\overline{\text{INT}}$
# 5	SDO	# 6	V <sub>SS</sub>

**PIN DESCRIPTION**

Symbol	Pin #	Description
CLKOE	1	CLKOUT enable/disable pin; enable is active HIGH
V <sub>DD</sub>	2	Positive supply voltage; positive or negative steps in supply voltage may affect oscillator performance recommend 10 nF decoupling capacitor close to device
CLKOUT	3	Clock Output pin; open-drain
SCL	4	Serial Clock Input pin; may float when CE inactive
SDO	5	Serial Data Output pin; push-pull; high-impedance when not driving; can be connected to SDI for single-wire data line
V <sub>SS</sub>	6	Ground
$\overline{\text{INT}}$	7	Interrupt output pin; open-drain; active LOW
CE	8	Chip Enable input; active HIGH; with internal pull-down
V <sub>BACKUP</sub>	9	Backup Supply Voltage; tie to GND when not using a backup supply voltage
SDI	10	Serial Data Input pin; may float when CE inactive